|  |  |
| --- | --- |
| MEMÓRIA PRINCIPAL | |
| 0000 | 100 |
| 0001 | 110 |
| 0010 | 120 |
| 0011 | 220 |
| 0100 | 140 |
| 0101 | 150 |
| 0110 | 160 |
| 0111 | 170 |
| 1000 | 180 |
| 1001 | 190 |
| 1010 | 200 |
| 1011 | 210 |
| 1100 | 220 |
| 1101 | 230 |
| 1110 | 240 |
| 1111 | 250 |

PROCESSADOR

lw r0 0

lw r2 13

sw r2 3

lw r1 2

sw r1 0

lw r3 6

sw r2 15

sw r0 11

lw r2 5

lw r1 12

|  |  |
| --- | --- |
| r0 | 100 |
| r1 |  |
| r2 |  |
| r3 |  |

|  |  |
| --- | --- |
| Memória CACHE | |
| 0 0 00  C D tag | 100 |
| 110 |
| 0  C D tag |  |
|  |
| 1  C D tag |  |
|  |
| 1  C D tag |  |
|  |

|  |  |
| --- | --- |
| 4MEMÓRIA PRINCIPAL | |
| 0000 | 100 |
| 0001 | 110 |
| 0010 | 120 |
| 0011 | 130 |
| 0100 | 140 |
| 0101 | 150 |
| 0110 | 160 |
| 0111 | 170 |
| 1000 | 180 |
| 1001 | 190 |
| 1010 | 200 |
| 1011 | 210 |
| 1100 | 220 |
| 1101 | 230 |
| 1110 | 240 |
| 1111 | 250 |

|  |  |
| --- | --- |
| MEMÓRIA PRINCIPAL | |
| 0000 | 100 |
| 0001 | 110 |
| 0010 | 120 |
| 0011 | 130 |
| 0100 | 140 |
| 0101 | 150 |
| 0110 | 160 |
| 0111 | 170 |
| 1000 | 180 |
| 1001 | 190 |
| 1010 | 200 |
| 1011 | 210 |
| 1100 | 220 |
| 1101 | 230 |
| 1110 | 240 |
| 1111 | 250 |

|  |  |
| --- | --- |
| Memória CACHE | |
| 0 0 00  C D tag  (LRU) | 100 |
| 110 |
| 0 0 11  C D tag | 220 |
| 230 |
| 1 1 00  C D tag | 120 |
| 230 |
| 1  C D tag |  |
|  |

|  |  |
| --- | --- |
| Memória CACHE | |
| 0 0 00  C D tag  (LRU) | 100 |
| 110 |
| 0 0 11  C D tag | 220 |
| 230 |
| 1  C D tag |  |
|  |
| 1  C D tag |  |
|  |

PROCESSADOR

lw r0 0

lw r2 13

sw r2 3

lw r1 2

sw r1 0

lw r3 6

sw r2 15

sw r0 11

lw r2 5

lw r1 12

|  |  |
| --- | --- |
| r0 | 100 |
| r1 |  |
| r2 | 230 |
| r3 |  |

PROCESSADOR

lw r0 0

lw r2 13

sw r2 3

lw r1 2

sw r1 0

lw r3 6

sw r2 15

sw r0 11

lw r2 5

lw r1 12

|  |  |
| --- | --- |
| r0 | 100 |
| r1 |  |
| r2 | 230 |
| r3 |  |

|  |  |
| --- | --- |
| MEMÓRIA PRINCIPAL | |
| 0000 | 100 |
| 0001 | 110 |
| 0010 | 120 |
| 0011 | 130 |
| 0100 | 140 |
| 0101 | 150 |
| 0110 | 160 |
| 0111 | 170 |
| 1000 | 180 |
| 1001 | 190 |
| 1010 | 200 |
| 1011 | 210 |
| 1100 | 220 |
| 1101 | 230 |
| 1110 | 240 |
| 1111 | 250 |

|  |  |
| --- | --- |
| MEMÓRIA PRINCIPAL | |
| 0000 | 100 |
| 0001 | 110 |
| 0010 | 120 |
| 0011 | 130 |
| 0100 | 140 |
| 0101 | 150 |
| 0110 | 160 |
| 0111 | 170 |
| 1000 | 180 |
| 1001 | 190 |
| 1010 | 200 |
| 1011 | 210 |
| 1100 | 220 |
| 1101 | 230 |
| 1110 | 240 |
| 1111 | 250 |

PROCESSADOR

lw r0 0

lw r2 13

sw r2 3

lw r1 2

sw r1 0

lw r3 6

sw r2 15

sw r0 11

lw r2 5

lw r1 12

|  |  |
| --- | --- |
| r0 | 100 |
| r1 | 120 |
| r2 | 230 |
| r3 |  |

|  |  |
| --- | --- |
| Memória CACHE | |
| 0 0 00  C D tag  (LRU) | 100 |
| 110 |
| 0 0 11  C D tag | 220 |
| 230 |
| 1 1 00  C D tag | 120 |
| 230 |
| 1  C D tag |  |
|  |

|  |  |
| --- | --- |
| MEMÓRIA PRINCIPAL | |
| 0000 | 100 |
| 0001 | 110 |
| 0010 | 120 |
| 0011 | 130 |
| 0100 | 140 |
| 0101 | 150 |
| 0110 | 160 |
| 0111 | 170 |
| 1000 | 180 |
| 1001 | 190 |
| 1010 | 200 |
| 1011 | 210 |
| 1100 | 220 |
| 1101 | 230 |
| 1110 | 240 |
| 1111 | 250 |

|  |  |
| --- | --- |
| Memória CACHE | |
| 0 1 00  C D tag | 120 |
| 110 |
| 0 0 11  C D tag  (LRU) | 220 |
| 230 |
| 1 1 00  C D tag  (LRU) | 120 |
| 230 |
| 1 0 01  C D tag | 160 |
| 170 |

|  |  |
| --- | --- |
| Memória CACHE | |
| 0 1 00  C D tag | 120 |
| 110 |
| 0 0 11  C D tag  (LRU) | 220 |
| 230 |
| 1 1 00  C D tag | 120 |
| 230 |
| 1  C D tag |  |
|  |

PROCESSADOR

lw r0 0

lw r2 13

sw r2 3

lw r1 2

sw r1 0

lw r3 6

sw r2 15

sw r0 11

lw r2 5

lw r1 12

|  |  |
| --- | --- |
| r0 | 100 |
| r1 | 120 |
| r2 | 230 |
| r3 | 160 |

PROCESSADOR

lw r0 0

lw r2 13

sw r2 3

lw r1 2

sw r1 0

lw r3 6

sw r2 15

sw r0 11

lw r2 5

lw r1 12

|  |  |
| --- | --- |
| r0 | 100 |
| r1 | 120 |
| r2 | 230 |
| r3 |  |

|  |  |
| --- | --- |
| Memória CACHE | |
| 0 1 00  C D tag | 120 |
| 110 |
| 0 0 11  C D tag  (LRU) | 220 |
| 230 |
| 1 1 11  C D tag | 240 |
| 230 |
| 1 0 01  C D tag  (LRU) | 160 |
| 170 |

PROCESSADOR

lw r0 0

lw r2 13

sw r2 3

lw r1 2

sw r1 0

lw r3 6

sw r2 15

sw r0 11

lw r2 5

lw r1 12

|  |  |  |
| --- | --- | --- |
| r0 | 100 |  |
| r1 | 120 |  |
| r2 | 230 |  |
| r3 | 160 |  |

|  |  |
| --- | --- |
| MEMÓRIA PRINCIPAL | |
| 0000 | 100 |
| 0001 | 110 |
| 0010 | 120 |
| 0011 | 120 |
| 0100 | 140 |
| 0101 | 150 |
| 0110 | 160 |
| 0111 | 170 |
| 1000 | 180 |
| 1001 | 190 |
| 1010 | 200 |
| 1011 | 210 |
| 1100 | 220 |
| 1101 | 230 |
| 1110 | 240 |
| 1111 | 250 |

|  |  |
| --- | --- |
| MEMÓRIA PRINCIPAL | |
| 0000 | 100 |
| 0001 | 110 |
| 0010 | 120 |
| 0011 | 130 |
| 0100 | 140 |
| 0101 | 150 |
| 0110 | 160 |
| 0111 | 170 |
| 1000 | 180 |
| 1001 | 190 |
| 1010 | 200 |
| 1011 | 210 |
| 1100 | 120 |
| 1101 | 230 |
| 1110 | 240 |
| 1111 | 250 |

|  |  |
| --- | --- |
| MEMÓRIA PRINCIPAL | |
| 0000 | 100 |
| 0001 | 110 |
| 0010 | 120 |
| 0011 | 130 |
| 0100 | 140 |
| 0101 | 150 |
| 0110 | 160 |
| 0111 | 170 |
| 1000 | 180 |
| 1001 | 190 |
| 1010 | 200 |
| 1011 | 210 |
| 1100 | 120 |
| 1101 | 230 |
| 1110 | 240 |
| 1111 | 250 |

|  |  |
| --- | --- |
| MEMÓRIA PRINCIPAL | |
| 0000 | 100 |
| 0001 | 110 |
| 0010 | 120 |
| 0011 | 130 |
| 0100 | 140 |
| 0101 | 150 |
| 0110 | 160 |
| 0111 | 170 |
| 1000 | 180 |
| 1001 | 190 |
| 1010 | 200 |
| 1011 | 210 |
| 1100 | 120 |
| 1101 | 230 |
| 1110 | 240 |
| 1111 | 250 |

|  |  |
| --- | --- |
| Memória CACHE | |
| 0 1 00  C D tag  (LRU) | 120 |
| 110 |
| 0 0 01  C D tag | 140 |
| 150 |
| 1 1 11  C D tag  (LRU) | 240 |
| 230 |
| 1 1 10  C D tag | 200 |
| 100 |

PROCESSADOR

lw r0 0

lw r2 13

sw r2 3

lw r1 2

sw r1 0

lw r3 6

sw r2 15

sw r0 11

lw r2 5

lw r1 12

|  |  |  |
| --- | --- | --- |
| r0 | 100 |  |
| r1 | 120 |  |
| r2 | 230 |  |
| r3 | 160 |  |

|  |  |
| --- | --- |
| Memória CACHE | |
| 0 1 00  C D tag | 120 |
| 110 |
| 0 0 11  C D tag  (LRU) | 220 |
| 230 |
| 1 1 11  C D tag  (LRU) | 240 |
| 230 |
| 1 1 10  C D tag | 200 |
| 100 |

PROCESSADOR

lw r0 0

lw r2 13

sw r2 3

lw r1 2

sw r1 0

lw r3 6

sw r2 15

sw r0 11

lw r2 5

lw r1 12

|  |  |  |
| --- | --- | --- |
| r0 | 100 |  |
| r1 | 120 |  |
| r2 | 150 |  |
| r3 | 160 |  |

|  |  |
| --- | --- |
| Memória CACHE | |
| 0 0 11  C D tag | 120 |
| 230 |
| 0 0 01  C D tag  (LRU) | 140 |
| 150 |
| 1 1 11  C D tag  (LRU) | 240 |
| 230 |
| 1 1 10  C D tag | 200 |
| 100 |

|  |  |
| --- | --- |
| MEMÓRIA PRINCIPAL | |
| 0000 | 120 |
| 0001 | 110 |
| 0010 | 120 |
| 0011 | 130 |
| 0100 | 140 |
| 0101 | 150 |
| 0110 | 160 |
| 0111 | 170 |
| 1000 | 180 |
| 1001 | 190 |
| 1010 | 200 |
| 1011 | 210 |
| 1100 | 120 |
| 1101 | 230 |
| 1110 | 240 |
| 1111 | 250 |

PROCESSADOR

lw r0 0

lw r2 13

sw r2 3

lw r1 2

sw r1 0

lw r3 6

sw r2 15

sw r0 11

lw r2 5

lw r1 12

|  |  |  |
| --- | --- | --- |
| r0 | 100 |  |
| r1 | 120 |  |
| r2 | 150 |  |
| r3 | 160 |  |